

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1	"5337318".pn.	USPAT	OR	OFF	2004/12/14 07:41
L6	1	"5337318".pn. and (memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and ((pass or test) with column) and ((pass or test) with (row near5 address)) and ((spare or redundant or redundancy or replacement) near4 (column or row))	USPAT	OR	ON	2004/12/14 10:11
L11	3	("4339657" "4414665" "5224101").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2004/12/14 07:49
L12	15	("5337318").URPN.	USPAT	OR	OFF	2004/12/14 07:49
L13	0	("4339657" "4414665" "5224101").PN. and (memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and ((first or initial or primary or beginning) near3 (pass or test) with column) and ((second or subsequent or following or "next" or secondary or later) near3 (pass or test) with (row near5 address)) and (self\$test\$3 or self\$examin\$5 or bist or (built adj self adj test)) and ((spare or redundant or redundancy or replacement) near4 (column or row))	USPAT; USOCR	OR	ON	2004/12/14 07:49
L14	1	("5337318").URPN. and (memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and ((first or initial or primary or beginning) near3 (pass or test) with column) and ((second or subsequent or following or "next" or secondary or later) near3 (pass or test) with (row near5 address)) and (self\$test\$3 or self\$examin\$5 or bist or (built adj self adj test)) and ((spare or redundant or redundancy or replacement) near4 (column or row))	USPAT	OR	ON	2004/12/14 08:00
L15	0	("4339657" "4414665" "5224101").PN. and (memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and ((pass or test) with column) and ((pass or test) with (row near5 address)) and (self\$test\$3 or self\$examin\$5 or bist or (built adj self adj test)) and ((spare or redundant or redundancy or replacement) near4 (column or row))	USPAT; USOCR	OR	ON	2004/12/14 07:50

L16	0	("4339657" "4414665" "5224101").PN. and (memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and ((pass or test) with column) and ((pass or test) with (row near5 address)) and ((spare or redundant or redundancy or replacement) near4 (column or row))	USPAT; USOCR	OR	ON	2004/12/14 07:50
L17	1	e\$fuse near5 macro	US-PGPUB; USPAT	OR	ON	2004/12/14 08:57
L18	556	((failed or failure) near3 address near3 (register or memory))	US-PGPUB; USPAT	OR	ON	2004/12/14 08:58
L19	48	(memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and ((pass or test) with column) and ((pass or test) with (row near5 address)) and ((spare or redundant or redundancy or replacement) near4 (column or row)) and ((failed or failure) near3 address near3 (register or memory))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/12/14 08:59
L20	5	(memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and (first near5 (pass or test) with column) and (second near5 (pass or test) with (row near5 address)) and ((spare or redundant or redundancy or replacement) near4 (column or row)) and ((failed or failure) near3 address near3 (register or memory))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/12/14 09:00
S1	14	("4701887" "5469389" "5506807" "5621691" "5831915" "5896328" "5999450" "6115300" "6134142" "6327197" "6366508" "6426902" "6445626" "6571348").pn.	USPAT	OR	OFF	2004/12/14 07:40
S2	186	360/26.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/07 09:50
S3	104	360/47.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/07 09:41
S5	383	360/39.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/07 09:42
S6	591	365/185.09.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/07 09:43
S7	3446	365/200.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/07 09:49
S8	423	702/85.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/07 09:49
S9	304	702/108.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/07 09:49
S10	490	702/117.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/07 09:49
S11	205	702/118.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/07 09:49

S12	63	714/100.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/07 09:49
S13	1329	714/6.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/07 09:49
S14	198	714/12.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/07 09:49
S15	650	714/758.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/07 09:50
S16	418	714/763.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/07 09:50
S17	0	360/26.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory)))	US-PGPUB; USPAT	OR	ON	2004/12/07 09:55
S18	2	360/47.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory)))	US-PGPUB; USPAT	OR	ON	2004/12/07 10:03
S19	4	360/53.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory)))	US-PGPUB; USPAT	OR	ON	2004/12/07 10:05
S20	0	360/39.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory)))	US-PGPUB; USPAT	OR	ON	2004/12/07 09:55
S21	75	365/185.09.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory)))	US-PGPUB; USPAT	OR	ON	2004/12/07 10:05
S22	1016	365/200.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory)))	US-PGPUB; USPAT	OR	ON	2004/12/07 10:06
S23	0	702/85.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory)))	US-PGPUB; USPAT	OR	ON	2004/12/07 09:56
S24	0	702/108.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory)))	US-PGPUB; USPAT	OR	ON	2004/12/07 09:56

S25	3	702/117.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory)))	US-PGPUB; USPAT	OR	ON	2004/12/07 10:06
S26	2	702/118.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory)))	US-PGPUB; USPAT	OR	ON	2004/12/07 10:01
S27	33	714/6.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory)))	US-PGPUB; USPAT	OR	ON	2004/12/07 10:06
S28	0	714/12.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory)))	US-PGPUB; USPAT	OR	ON	2004/12/07 09:57
S29	0	714/58.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory)))	US-PGPUB; USPAT	OR	ON	2004/12/07 09:57
S30	0	714/63.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory)))	US-PGPUB; USPAT	OR	ON	2004/12/07 09:58
S31	0	714/100.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory)))	US-PGPUB; USPAT	OR	ON	2004/12/07 09:58
S32	0	360/47.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and (self\$test or self\$examination or bist or (built\$in adj self\$test))	US-PGPUB; USPAT	OR	ON	2004/12/07 10:05
S33	0	360/53.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and (self\$test or self\$examination or bist or (built\$in adj self\$test))	US-PGPUB; USPAT	OR	ON	2004/12/07 10:05
S34	4	365/185.09.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and (self\$test or self\$examination or bist or (built\$in adj self\$test))	US-PGPUB; USPAT	OR	ON	2004/12/07 10:07

S35	75	365/200.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and (self\$test or self\$examination or bist or (built\$in adj self\$test))	US-PGPUB; USPAT	OR	ON	2004/12/07 10:12
S36	0	702/117.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and (self\$test or self\$examination or bist or (built\$in adj self\$test))	US-PGPUB; USPAT	OR	ON	2004/12/07 10:06
S37	0	702/118.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and (self\$test or self\$examination or bist or (built\$in adj self\$test))	US-PGPUB; USPAT	OR	ON	2004/12/07 10:06
S38	3	714/6.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and (self\$test or self\$examination or bist or (built\$in adj self\$test))	US-PGPUB; USPAT	OR	ON	2004/12/07 10:08
S39	11	365/200.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and (self\$test or self\$examination or bist or (built\$in adj self\$test)) and ((first or initial or primary or beginning) near5 pass)	US-PGPUB; USPAT	OR	ON	2004/12/07 10:13
S40	9	365/200.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and (self\$test or self\$examination or bist or (built\$in adj self\$test)) and ((first or initial or primary or beginning) near5 pass) and ((second or subsequent or following or "next" or secondary or later) near5 pass)	US-PGPUB; USPAT	OR	ON	2004/12/07 10:15
S41	13	"365"/\$.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and (self\$test or self\$examination or bist or (built\$in adj self\$test)) and ((first or initial or primary or beginning) near5 pass) and ((second or subsequent or following or "next" or secondary or later) near5 pass)	US-PGPUB; USPAT	OR	ON	2004/12/07 10:16

S42	0	"702"/\$.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and (self\$test or self\$examination or bist or (built\$in adj self\$test)) and ((first or initial or primary or beginning) near5 pass) and ((second or subsequent or following or "next" or secondary or later) near5 pass)	US-PGPUB; USPAT	OR	ON	2004/12/07 10:15
S43	12	"714"/\$.ccls. and ((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and (self\$test or self\$examination or bist or (built\$in adj self\$test)) and ((first or initial or primary or beginning) near5 pass) and ((second or subsequent or following or "next" or secondary or later) near5 pass)	US-PGPUB; USPAT	OR	ON	2004/12/07 10:16
S44	36	((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and (self\$test or self\$examination or bist or (built\$in adj self\$test)) and ((first or initial or primary or beginning) near5 pass) and ((second or subsequent or following or "next" or secondary or later) near5 pass)	US-PGPUB; USPAT	OR	ON	2004/12/07 10:28
S45	0	((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and (self\$test or self\$examination or bist or (built\$in adj self\$test)) and ((first or initial or primary or beginning) near5 pass) and ((second or subsequent or following or "next" or secondary or later) near5 pass)	USOCR	OR	ON	2004/12/07 10:28
S46	0	((row or column) near5 (redundant or redundancy)) and (ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and (self\$test or self\$examination or bist or (built\$in adj self\$test)) and ((first or initial or primary or beginning) near5 pass) and ((second or subsequent or following or "next" or secondary or later) near5 pass)	EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/12/07 10:29

S47	0	((row or column) near5 (redundant or redundancy)) and (memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and (self\$test or self\$examination or bist or (built\$in adj self\$test)) and ((first or initial or primary or beginning) near5 pass) and ((second or subsequent or following or "next" or secondary or later) near5 pass)	EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/12/07 10:29
S48	0	((method\$5 or process or procedure) with (self\$test\$3 or self\$exam\$7) with allocat\$3 with (repair\$3 or reparation) with ((random adj access adj memory) or memory or ram or dram or sram or ((dynamic or static) near3 (random adj access adj memory)) with ((spare or redundant or excess) near4 (column or row)))) and ((perform\$4 with (first or initial or primary or beginning) with (second\$3 or later or subsequent or "next" or following) with (pass or passes) with (self\$test\$3 or self\$exam\$7) with ((random adj access adj memory) or memory or ram or dram or sram or ((dynamic or static) near3 (random adj access adj memory)))) and ((first or initial or primary or beginning) with (pass or passes) with (self\$test\$3 or self\$exam\$7) with determin\$5 with (worst or most) with fail\$3 with column with ((random adj access adj memory) or memory or ram or dram or sram or ((dynamic or static) near3 (random adj access adj memory) with (after or later or subsequent or "next" or following) with complet\$3 with (first or initial or primary or beginning) with (pass or passes) with (self\$test\$3 or self\$exam\$7) with (assign\$4 or allocat\$3) with (spare or redundant or excess) with column replac\$4 with (worst or most) with fail\$3 with column))) and ((second\$3 or later or subsequent or "next" or following) with (pass or passes) with (self\$test\$3 or self\$exam\$7) with determin\$5 with (particular or specific or unique) with fail\$3 with row with memory with (addresses or address)) and ((after or later or subsequent or "next" or following) with complet\$3 with (second\$3 or later or subsequent or "next" or following) with (pass or passes) with (self\$test\$3 or self\$exam\$7) with allocat\$3 with (spare or redundant or excess) with row with replac\$4 with fail\$3 with row)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR~	ON	2004/12/07 10:53

S49	21	("6795942" "6768694" "6691252" "6686747" "6639857" "6634003" "6625746" "6611458" "6546503" "6507524" "6496947" "6445626" "6408401" "6373758" "6366508" "6259637" "6263295" "5961653" "5920515" "5841712" "5815437" "5784323" "5680544" "5299203" "4833677" "20020196029" "20020108073" "20030076125" "20040066695" "20040136257" "2004204891").pn. and ((row or column) near5 (redundant or redundancy)) and (memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and (self\$test or self\$examination or bist or (built\$in adj self\$test)) and ((first or initial or primary or beginning) near5 pass) and ((second or subsequent or following or "next" or secondary or later) near5 pass)	US-PGPUB; USPAT	OR	ON	2004/12/07 14:39
S50	0	(memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and ((first or initial or primary or beginning) near3 (pass or passes)) and ((second or subsequent or following or "next" or secondary or later) near3 (pass or passes)) and ((worst or most) near3 fail\$3 near3 column) and ((worst or most) near3 fail\$3 near3 row) and ((allocat\$3 or assign\$4 or designat\$3) near4 (spare or redundant or extra or excess or replac\$5) near4 (column or row))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/12/08 07:46
S51	16	(memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and ((first or initial or primary or beginning) near3 (pass or passes)) and ((second or subsequent or following or "next" or secondary or later) near3 (pass or passes)) and (fail\$3 near3 column) and (fail\$3 near3 row) and ((spare or redundant or extra or excess or replac\$5) near4 (column or row))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/12/07 14:50
S52	5	("4460997" "4460999" "4736373" "5355340" "6259637").pn.	USPAT	OR	OFF	2004/12/08 08:56
S53	976	360/53.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/08 07:29
S54	3	("6373758" "6691252" "20020108073"). pn.	US-PGPUB; USPAT	OR	OFF	2004/12/08 07:41

S55	8	("6373758" "6691252" "20020108073" "4460997" "4460999" "4736373" "5355340" "6259637" "20020108073").pn. and ((first or initial or beginning) near3 (pass or passes or test or testing)) and ((second or following or subsequent or after or later) near3 (pass or passes or test or testing))	US-PGPUB; USPAT	OR	ON	2004/12/08 07:42
S56	28	(memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and ((first or initial or primary or beginning) near3 (pass or test) with column) and ((second or subsequent or following or "next" or secondary or later) near3 (pass or test) with (row near5 address)) and (self\$test\$3 or self\$examin\$5 or bist or (built adj self adj test))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/12/08 09:20
S57	14	(memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and ((first or initial or primary or beginning) near3 (pass or test) with column) and ((second or subsequent or following or "next" or secondary or later) near3 (pass or test) with (row near5 address)) and (self\$test\$3 or self\$examin\$5 or bist or (built adj self adj test)) and ((spare or redundant or redundancy or replacement) near4 (column or row))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/12/08 08:04
S58	28	(semi\$conductor or microchip or microprocessor or microelectronic or memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and ((first or initial or primary or beginning) near3 (pass or test) with column) and ((second or subsequent or following or "next" or secondary or later) near3 (pass or test) with (row near5 address)) and (self\$test\$3 or self\$examin\$5 or bist or bist or (built adj self adj (test or repair)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/12/08 09:21
S59	1	((method or methodology or process or procedure or algorithm) same (self\$test\$3 or self\$exam\$7 or bist or (built adj self adj (test or repair)))) and ((spare or redundant or redundancy) near4 (column or row)) and e\$fuse and (far or (failing adj address adj register)) and ((first or initial or beginning) near3 pass) and ((second or subsequent or "next" or following or later) near3 pass)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/12/08 12:12

S60	12	((method or methodology or process or procedure or algorithm) same (self\$test\$3 or self\$exam\$7 or bist or (built adj self adj (test or repair)))) and ((spare or redundant or redundancy) near4 (column or row)) and (far or (failing adj address adj register)) and ((first or intial or beginning) near3 pass) and ((second or subsequent or "next" or following or later) near3 pass)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/12/08 12:18
S61	4	((self\$test\$3 or self\$exam\$7 or bist or (built adj self adj (test or repair)))) and (first with pass with ((redundant or spare or redundancy) near4 column)) and (((second or subsequent) with pass) with ((redundant or spare or redundancy) near4 row))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2004/12/08 13:59
S62	8	eustis-steven-m.in.	US-PGPUB; USPAT	OR	OFF	2004/12/08 14:07
S63	2	mondal-krishnendu.in.	US-PGPUB; USPAT	OR	OFF	2004/12/08 14:00
S64	27	ouellette-michael-r.in.	US-PGPUB; USPAT	OR	OFF	2004/12/08 14:02
S65	6	hughes-brian-william.in.	US-PGPUB; USPAT	OR	OFF	2004/12/08 14:09
S66	12	howlett-warren-kurt.in.	US-PGPUB; USPAT	OR	OFF	2004/12/08 14:14
S67	2721	365/201.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/08 14:15
S68	496	714/7.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/08 14:15
S69	389	714/10.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/08 14:15
S70	490	714/11.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/08 14:15
S71	5	365/201.ccls. and (memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and ((first or initial or primary or beginning) near3 (pass or test) with column) and ((second or subsequent or following or "next" or secondary or later) near3 (pass or test) with (row near5 address)) and (self\$test\$3 or self\$examin\$5 or bist or (built adj self adj test)) and ((spare or redundant or redundancy or replacement) near4 (column or row))	US-PGPUB; USPAT	OR	ON	2004/12/08 14:17

S72	4	714/7.ccls. and (memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and ((first or initial or primary or beginning) near3 (pass or test) with column) and ((second or subsequent or following or "next" or secondary or later) near3 (pass or test) with (row near5 address)) and (self\$test\$3 or self\$examin\$5 or bist or (built adj self adj test)) and ((spare or redundant or redundancy or replacement) near4 (column or row))	US-PGPUB; USPAT	OR	ON	2004/12/08 14:51
S73	0	714/10.ccls. and (memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and ((first or initial or primary or beginning) near3 (pass or test) with column) and ((second or subsequent or following or "next" or secondary or later) near3 (pass or test) with (row near5 address)) and (self\$test\$3 or self\$examin\$5 or bist or (built adj self adj test)) and ((spare or redundant or redundancy or replacement) near4 (column or row))	US-PGPUB; USPAT	OR	ON	2004/12/08 14:16
S74	0	714/11.ccls. and (memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and ((first or initial or primary or beginning) near3 (pass or test) with column) and ((second or subsequent or following or "next" or secondary or later) near3 (pass or test) with (row near5 address)) and (self\$test\$3 or self\$examin\$5 or bist or (built adj self adj test)) and ((spare or redundant or redundancy or replacement) near4 (column or row))	US-PGPUB; USPAT	OR	ON	2004/12/08 14:16
S75	7	"365"/\$.ccls. and (memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and ((first or initial or primary or beginning) near3 (pass or test) with column) and ((second or subsequent or following or "next" or secondary or later) near3 (pass or test) with (row near5 address)) and (self\$test\$3 or self\$examin\$5 or bist or (built adj self adj test)) and ((spare or redundant or redundancy or replacement) near4 (column or row))	US-PGPUB; USPAT	OR	ON	2004/12/08 14:52

S76	8	"714"/\$.ccls. and (memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and ((first or initial or primary or beginning) near3 (pass or test) with column) and ((second or subsequent or following or "next" or secondary or later) near3 (pass or test) with (row near5 address)) and (self\$test\$3 or self\$examin\$5 or bist or (built adj self adj test)) and ((spare or redundant or redundancy or replacement) near4 (column or row))	US-PGPUB; USPAT	OR	ON	2004/12/08 14:53
S77	0	"702"/\$.ccls. and (memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and ((first or initial or primary or beginning) near3 (pass or test) with column) and ((second or subsequent or following or "next" or secondary or later) near3 (pass or test) with (row near5 address)) and (self\$test\$3 or self\$examin\$5 or bist or (built adj self adj test)) and ((spare or redundant or redundancy or replacement) near4 (column or row))	US-PGPUB; USPAT	OR	ON	2004/12/08 14:52
S78	429	714/710.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/09 08:55
S79	227	714/711.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/09 08:55
S80	2	714/710.ccls. and (memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and ((first or initial or primary or beginning) near3 (pass or test) with column) and ((second or subsequent or following or "next" or secondary or later) near3 (pass or test) with (row near5 address)) and (self\$test\$3 or self\$examin\$5 or bist or (built adj self adj test)) and ((spare or redundant or redundancy or replacement) near4 (column or row))	US-PGPUB; USPAT	OR	ON	2004/12/09 12:36
S81	1	714/711.ccls. and (memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and ((first or initial or primary or beginning) near3 (pass or test) with column) and ((second or subsequent or following or "next" or secondary or later) near3 (pass or test) with (row near5 address)) and (self\$test\$3 or self\$examin\$5 or bist or (built adj self adj test)) and ((spare or redundant or redundancy or replacement) near4 (column or row))	US-PGPUB; USPAT	OR	ON	2004/12/09 08:58
S82	83	714/41.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/09 12:36

S83	544	714/42.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/09 12:36
S84	718	714/733.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/09 12:36
S85	346	714/734.ccls.	US-PGPUB; USPAT	OR	OFF	2004/12/09 12:36
S86	0	714/41.ccls. and (memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and ((first or initial or primary or beginning) near3 (pass or test) with column) and ((second or subsequent or following or "next" or secondary or later) near3 (pass or test) with (row near5 address)) and (self\$test\$3 or self\$examin\$5 or bist or (built adj self adj test)) and ((spare or redundant or redundancy or replacement) near4 (column or row))	US-PGPUB; USPAT	OR	ON	2004/12/09 12:37
S87	2	714/42.ccls. and (memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and ((first or initial or primary or beginning) near3 (pass or test) with column) and ((second or subsequent or following or "next" or secondary or later) near3 (pass or test) with (row near5 address)) and (self\$test\$3 or self\$examin\$5 or bist or (built adj self adj test)) and ((spare or redundant or redundancy or replacement) near4 (column or row))	US-PGPUB; USPAT	OR	ON	2004/12/09 12:38
S88	1	714/733.ccls. and (memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and ((first or initial or primary or beginning) near3 (pass or test) with column) and ((second or subsequent or following or "next" or secondary or later) near3 (pass or test) with (row near5 address)) and (self\$test\$3 or self\$examin\$5 or bist or (built adj self adj test)) and ((spare or redundant or redundancy or replacement) near4 (column or row))	US-PGPUB; USPAT	OR	ON	2004/12/09 12:39
S89	0	714/734.ccls. and (memory or ram or (random adj access adj memory) or dram or sram or ((dynamic or static) near5 (random adj access adj memory))) and ((first or initial or primary or beginning) near3 (pass or test) with column) and ((second or subsequent or following or "next" or secondary or later) near3 (pass or test) with (row near5 address)) and (self\$test\$3 or self\$examin\$5 or bist or (built adj self adj test)) and ((spare or redundant or redundancy or replacement) near4 (column or row))	US-PGPUB; USPAT	OR	ON	2004/12/09 12:37